

Features

- High efficiency and excellent thermal performance
- Output remote sense
- Output over-voltage, over-current, short-circuit and over-temperature protections
- Fixed frequency operation
- 1,500Vdc input to output isolation
- Wide operating temperature range from -40°C to +100°C
- UL 62368-1 2nd edition recognized
- Qualification/Screening satisfy: IPC-9592



Absolute Maximum Ratings

Excessive stresses over these absolute maximum ratings can cause damage to the converter. Operation should be limited to the conditions outlined under the Electrical Specifications.

Parameter	Min	Max	Unit
Input Voltage (continuous)	-0.5	40	Vdc
Input Voltage (< 100ms, operating)	-	50	Vdc
Input Voltage (continuous, non-operating)	-	50	Vdc
Storage Temperature	-55	125	°C

Part Numbering System

MYR	S1	□□□	□	□□□	□	□	□	(□)	-	□
Family Name	Input Voltage	Output Voltage	Enabling Logic	Rated Output Current	Pin Length	Electrical Options	Packaging	Suffix	-	Operating Temperature *Grade (°C)
	S1: 9-36V	Unit: 0.1V 050: 5V	P: Positive N: Negative	Unit: A 003: 3A	K: 0.095" N: 0.130" R: 0.165"	0: Latch off 2: Auto-restart	P: Without flange F: With flange	Variation code	-	C: -20 to +100 H: -40 to +100

* Operating temperature is the temperature measured at the center of the baseplate.

Available Codes:

Output Voltage	3.3V	5V	8V	12V	28V
Output Current	7A	3A, 5A	3A	2A	1A

MYRS1 Series 9 – 36V Input Thirty-Second Brick Converters

Electrical Specifications

These specifications are valid over the converter's full range of input voltage, resistive load, and operating temperature unless noted otherwise.

Input Specifications

Parameter	Min	Typical	Max	Unit
Input Voltage	9	18	36	Vdc
Input Turn-on Voltage Threshold	8.0	8.5	9.0	V
Input Turn-off Voltage Threshold	6.5	7.0	7.5	V

Output Specifications

Parameter	Min	Typical	Max	Unit
Output Voltage Set Point Accuracy (typical Vin; full load; Ta = 25°C)	-1.5	-	+1.5	%Vo
Output Voltage Set Point Accuracy (over all conditions)	-3	-	+3	%Vo
Output Regulation:				
Line Regulation (full range input voltage, 1/2 full load)	-	0.2	0.5	%Vo
Load Regulation (full range load, typical Vin)	-	0.2	0.5	
Temperature (Ta = -40°C to 85 °C)	-	0.1	-	
Output Trim Range in % of typical Vo	80	-	110	%

General Specifications

Parameter	Min	Typical	Max	Unit
Remote Enable				
Logic Low:				
ION/OFF = 1.0mA	0	-	1.2	V
VON/OFF = 0.0V	-	-	1.0	mA
Logic High:				
ION/OFF = 0.0µA	3.5	-	15	V
Leakage Current	-	-	50	µA
Isolation Capacitance	-	1,200	-	pF
Insulation Resistance	10	-	-	MΩ

Module Specific Specifications

3.3V/7A Module (MYRS1033x007xxx(x)-x)

Parameter	Min	Typical	Max	Unit
Input Current	-	-	6	A
Quiescent Input Current (typical Vin)	-	75	150	mA
Standby Input Current	-	10	20	mA
Efficiency (typical Vin; full load, Ta = 25°C)	-	85.0	-	%
Output Voltage Set Point	-	3.3	-	V
Output Over Current Protection Set Point	8	10	11.9	A
Output Over Voltage Protection Set Point	3.8	4.5	5.5	V
Output Ripple Frequency	260	300	340	kHz
Output Ripple and Noise Voltage (Tested with 4x47µF ceramic output caps)				
RMS	-	-	20	mVrms
Peak-to-peak (20 MHz bandwidth, typical Vin)	-	-	70	mVp-p
Output Ripple and Noise Voltage (Tested without ceramic output caps)				
RMS	-	-	40	mVrms
Peak-to-peak (20 MHz bandwidth, typical Vin)	-	-	150	mVp-p
External Load Capacitance	-	-	3,000	µF

5V/3A Module (MYRS1050x003xxx(x)-x)

Parameter	Min	Typical	Max	Unit
Input Current	-	-	4	A
Quiescent Input Current (typical Vin)	-	100	150	mA
Standby Input Current	-	10	20	mA
Efficiency (typical Vin; full load, Ta = 25°C)	-	85.0	-	%
Output Voltage Set Point	-	5.0	-	V
Output Over Current Protection Set Point	3.3	4	4.8	A
Output Over Voltage Protection Set Point	6	6.5	7.5	V
Output Ripple Frequency	260	300	340	kHz
Output Ripple and Noise Voltage (Tested with 4x47uF ceramic output caps)				
RMS	-	-	15	mVrms
Peak-to-peak (20 MHz bandwidth, typical Vin)	-	-	50	mVp-p
Output Ripple and Noise Voltage (Tested without ceramic output caps)				
RMS	-	-	30	mVrms
Peak-to-peak (20 MHz bandwidth, typical Vin)	-	-	100	mVp-p
External Load Capacitance	-	-	2,200	µF

5V/5A Module (MYRS1050x005xxx(x)-x)

Parameter	Min	Typical	Max	Unit
Input Current	-	-	6	A
Quiescent Input Current (typical Vin)	-	100	150	mA
Standby Input Current	-	10	20	mA
Efficiency (typical Vin; full load, Ta = 25°C)	-	85.0	-	%
Output Voltage Set Point	-	5.0	-	V
Output Over Current Protection Set Point	5.5	6	8	A
Output Over Voltage Protection Set Point	6	6.5	7	V
Output Ripple Frequency	260	300	340	kHz
Output Ripple and Noise Voltage (Tested with 4x47uF ceramic output caps)				
RMS	-	-	20	mVrms
Peak-to-peak (20 MHz bandwidth, typical Vin)	-	-	80	mVp-p
Output Ripple and Noise Voltage (Tested without ceramic output caps)				
RMS	-	-	70	mVrms
Peak-to-peak (20 MHz bandwidth, typical Vin)	-	-	250	mVp-p
External Load Capacitance	-	-	2,200	µF

MYRS1 Series 9 – 36V Input Thirty-Second Brick Converters

8V/3A Module (MYRS1080x003xxx(x)-x)

Parameter	Min	Typical	Max	Unit
Input Current	-	-	6	A
Quiescent Input Current (typical Vin)	-	80	150	mA
Standby Input Current	-	10	20	mA
Efficiency (typical Vin; full load, Ta = 25°C)	-	85.0	-	%
Output Voltage Set Point	-	8.0	-	V
Output Over Current Protection Set Point	3.6	4.5	4.8	A
Output Over Voltage Protection Set Point	9.0	10.5	12	V
Output Ripple Frequency	260	300	340	kHz
Output Ripple and Noise Voltage (Tested with 4x47uF ceramic output caps)				
RMS	-	-	35	mVrms
Peak-to-peak (20 MHz bandwidth, typical Vin)	-	-	100	mVp-p
Output Ripple and Noise Voltage (Tested without ceramic output caps)				
RMS	-	-	50	mVrms
Peak-to-peak (20 MHz bandwidth, typical Vin)	-	-	200	mVp-p
External Load Capacitance	-	-	3,000	μF

12V/2A Module (MYRS1120x002xxx(x)-x)

Parameter	Min	Typical	Max	Unit
Input Current	-	-	6	A
Quiescent Input Current (typical Vin)	-	10	30	mA
Standby Input Current	-	5	10	mA
Efficiency (typical Vin; full load, Ta = 25°C)	-	84.0	-	%
Output Voltage Set Point	-	12.0	-	V
Output Over Current Protection Set Point	2.2	2.9	3.2	A
Output Over Voltage Protection Set Point	14	16	21	V
Output Ripple Frequency	260	300	340	kHz
Output Ripple and Noise Voltage (Tested with 4x47uF ceramic output caps)				
RMS	-	-	40	mVrms
Peak-to-peak (20 MHz bandwidth, typical Vin)	-	-	150	mVp-p
Output Ripple and Noise Voltage (Tested without ceramic output caps)				
RMS	-	-	50	mVrms
Peak-to-peak (20 MHz bandwidth, typical Vin)	-	-	200	mVp-p
External Load Capacitance	-	-	1,000	μF

28V/1A Module (MYRS1280x001xxx(x)-x)

Parameter	Min	Typical	Max	Unit
Input Current	-	-	6	A
Quiescent Input Current (typical Vin)	-	35	70	mA
Standby Input Current	-	5	10	mA
Efficiency (typical Vin; full load, Ta = 25°C)	-	84.0	-	%
Output Voltage Set Point	-	12.0	-	V
Output Over Current Protection Set Point	1.1	1.3	1.5	A
Output Over Voltage Protection Set Point	32	35	38	V
Output Ripple Frequency	260	300	340	kHz
Output Ripple and Noise Voltage (Tested with 4x47uF ceramic output caps)				
RMS	-	-	60	mVrms
Peak-to-peak (20 MHz bandwidth, typical Vin)	-	-	200	mVp-p
Output Ripple and Noise Voltage (Tested without ceramic output caps)				
RMS	-	-	80	mVrms
Peak-to-peak (20 MHz bandwidth, typical Vin)	-	-	250	mVp-p
External Load Capacitance	-	-	220	μF

Feature Descriptions

Remote ON/OFF

The converter can be turned on and off by changing the voltage between the ON/OFF pin and Vin(-). The MYRS1 Series of converters are available with factory selectable positive logic and negative logic.

For the negative control logic, the converter is ON when the ON/OFF pin is at a logic low level and OFF when the ON/OFF pin is at a logic high level. For the positive control logic, the converter is ON when the ON/OFF pin is at a logic high level and OFF when the ON/OFF pin is at a logic low level.

With the internal pull-up circuitry, a simple external switch between the ON/OFF pin and Vin(-) can control the converter. A few example circuits for controlling the ON/OFF pin are shown in Figures 1, 2 and 3.

The logic low level is from 0V to 1.2V and the maximum sink current during logic low is 1mA. The external switch must be capable of maintaining a logic-low level while sinking up to this current. The logic high level is from 3.5V to 15V. The converter has an internal pull-up circuit that ensures the ON/OFF pin at a high logic level when the leakage current at ON/OFF pin is no greater than 50µA.

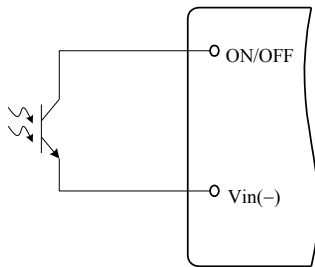


Figure 1. Opto Coupler Enable Circuit

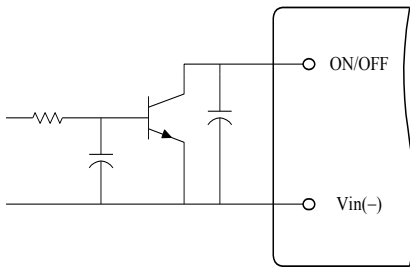


Figure 2. Open Collector Enable Circuit

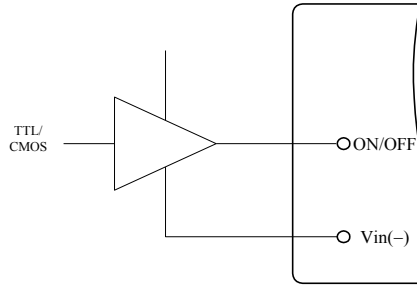


Figure 3. Direct Logic Drive

Remote SENSE

The remote SENSE pins are used to sense the voltage at the load point to accurately regulate the load voltage and eliminate the impact of the voltage drop in the power distribution path.

SENSE (+) and SENSE (-) pins should be connected between the points where voltage regulation is desired. The voltage between the SENSE pins and the output pins must not exceed the smaller of 0.5V or 10% of typical output voltage.

$$[V_{out (+)} - V_{out (-)}] - [SENSE (+) - SENSE (-)] < MIN \{0.5V, 10\%V_o\}$$

When remote sense is not used, the SENSE pins should be connected to their corresponding output pins. If the SENSE pins are left floating, the converter will deliver an output voltage slightly higher than its specified typical output voltage.

Output Voltage Adjustment (Trim)

The trim pin allows the user to adjust the output voltage set point. To increase the output voltage, an external resistor is connected between the TRIM pin and SENSE(+). To decrease the output voltage, an external resistor is connected between the TRIM pin and SENSE(-). The output voltage trim range is 80% to 110% of the specified typical output voltage.

The circuit configuration for trim down operation is shown in Figure 4. To decrease the output voltage, the value of the external resistor should be

$$R_{down} = \left(\frac{511}{\Delta} - 10.22 \right) (k\Omega)$$

Where

$$\Delta = \left(\frac{|V_{nom} - V_{adj}|}{V_{nom}} \right) \times 100$$

And

V_{nom} = Typical Output Voltage
 V_{adj} = Adjusted Output Voltage

The circuit configuration for trim up operation is shown in Figure 5. To increase the output voltage, the value of the resistor should be

$$R_{up} = \left(\frac{5.11V_o(100 + \Delta)}{1.225\Delta} - \frac{511}{\Delta} - 10.22 \right) (k\Omega)$$

Where

V_o = Typical Output Voltage

As the output voltage at the converter output terminals are higher than the specified typical level when using the trim up and/or remote sense functions, it is important to make sure that the voltage at the output terminals does not exceed the maximum power rating of the converter as given in the Specifications table.

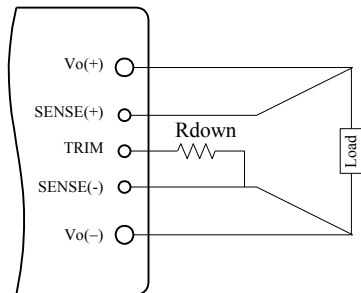


Figure 4. Circuit to Decrease Output Voltage

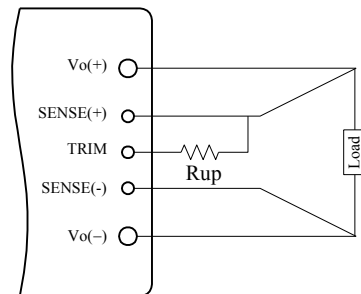


Figure 5. Circuit to Increase Output Voltage

Output Over-Current Protection (OCP)

This converter can be ordered in either latch-off or auto-restart version upon OCP, OVP, and OTP.

With the latch-off version, the converter will latch off when the load current exceeds the limit. The converter can be restarted by toggling the ON/OFF switch or recycling the input voltage.

With the auto-restart version, the converter will operate in a hiccup mode (repeatedly try to restart) until the cause of the over-current condition is cleared.

Output Over-Voltage Protection (OVP)

With the latch-off version, the converter will latch off when the output voltage exceeds the limit. The converter can be restarted by toggling the ON/OFF switch or recycling the input voltage.

With the auto-restart version, the converter will operate in a hiccup mode (repeatedly try to restart) until the cause of the over-voltage condition is cleared.

Over Temperature Protection (OTP)

With the latch-off version, the converter will shut down and latch off if an over-temperature condition is detected. The converter has a temperature sensor located at a carefully selected position, which represents the thermal condition of key components of the converter. The thermal shutdown circuit is designed to turn the converter off when the temperature at the sensor reaches 120°C. The module can be restarted by toggling the ON/OFF switch or recycling the input voltage.

With the auto-restart version, the converter will resume operation after the converter cools down.

Design Considerations

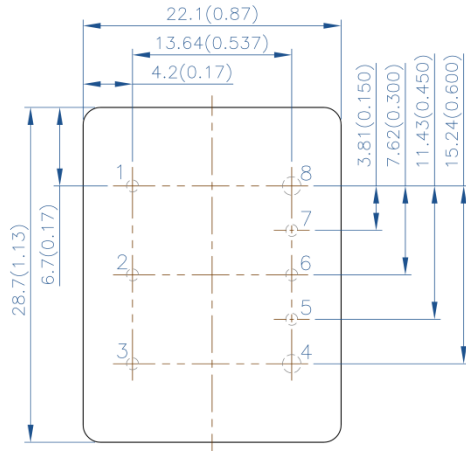
As with any DC/DC converter, the stability of the MYRS1 converters may be compromised if the source impedance is too high or inductive. It's desirable to keep the input source ac-impedance as low as possible. Although the converters are designed to be stable without adding external input capacitors for typical source impedance, it is recommended to add 220 μF low ESR electrolytic capacitors at the input of the converter for each 100W output power, which reduces the potential negative impact of the source impedance on the converter stability. These electrolytic capacitors should have sufficient RMS current rating over the operating temperature range.

The converter is designed to be stable without additional output capacitors. To further reduce the output voltage ripple or improve the transient response, additional output capacitors are often used in applications. When additional output capacitors are used, a combination of ceramic capacitors and tantalum/polymer capacitors shall be used to provide good filtering while assuring the stability of the converter.

MYRS1 Series 9 – 36V Input Thirty-Second Brick Converters

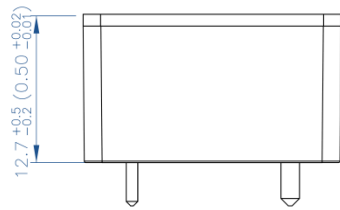
Mechanical Drawing

Without flange



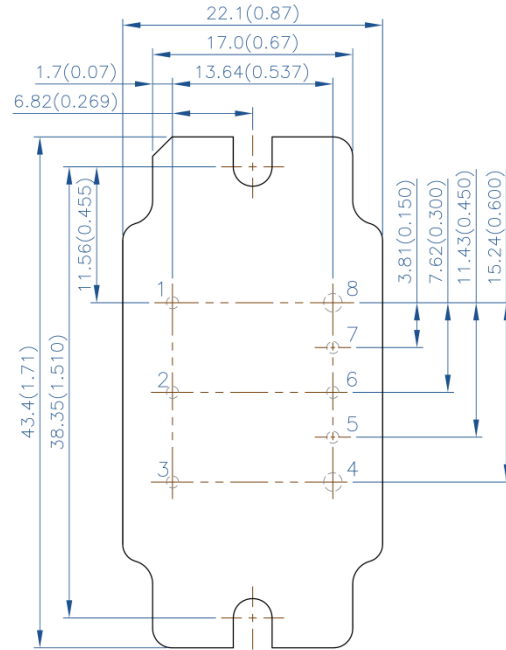
*Pin 1,2,3,5,6,7 are 1.02 (0.040) dia.
*Pin 4,8 are 1.57 (0.062) dia.

TOP VIEW



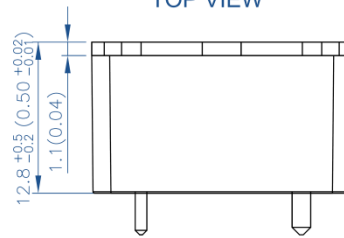
SIDE VIEW

With flange



*Pin 1,2,3,5,6,7 are 1.02 (0.040) dia.
*Pin 4,8 are 1.57 (0.062) dia.

TOP VIEW



SIDE VIEW

Pin	Name	Function
1	Vin(+)	Positive input voltage
2	ON/OFF	Remote control
3	Vin(-)	Negative input voltage
4	Vout(-)	Negative output voltage
5	SENSE(-)	Negative remote sense
6	TRIM	Output voltage adjustment
7	SENSE(+)	Positive remote sense
8	Vout(+)	Positive output voltage

Notes

- All dimensions in mm (inches)
Tolerances: .x ± .5 (.xx ± 0.02)
.xx ± .25 (.xxx ± 0.010)
- Input and function pins are 1.02mm (0.040") dia. with +/- 0.10mm (0.004") tolerance; the recommended diameter of the receiving hole is 1.42mm (0.056").
- Output pins are 1.57 mm (0.062") dia. with +/- 0.10mm (0.004") tolerance; the recommended diameter of the receiving hole is 1.98mm (0.078").
- All pins are coated with 90%/10% solder, Gold, or Matte Tin finish with Nickel under plating.
- Workmanship meets or exceeds IPC-A-610 Class II
- Torque applied on screw should not exceed 6in-lb (0.7Nm)
- Baseplate flatness tolerance is 0.10mm (0.004") TIR for surface.