

12V 1.5A 1x1 Isolated Converter



Features

- High Efficiency: 88.0% (12V/1.5A)
- Excellent thermal performance
- Industry standard footprint and pin out
- Robust stability
- Monotonic start-up
- Fixed frequency operation
- Basic Insulation, 2,250Vdc input to output isolation
- UL 62368-1 2nd edition recognized†

Options

- Auto-restart / Latch off after fault shutdown
- Negative / Positive enable logic
- Pin lengths

Part Numbering System

LRS	3	120	□	1A5	□	□	5	W
Series Name	Input Voltage	Output Voltage	Enabling Logic	Rated Output Current	Pin Length Options	Electrical Options	Mechanical Options Lead-free (ROHS-6 Compliant)	Suffix
	See suffix	Unit: 0.1V 120: 12V	P: Positive N: Negative	Unit: A 1A5: 1.5A	K: 0.110" N: 0.145" R: 0.180"	0: Latch off 2: Auto-restart	5: Open frame	W: 18-75V input range

† UL is a registered trademark of Underwriters Laboratory Inc.

Absolute Maximum Rating

Excessive stresses over these absolute maximum ratings can cause permanent damage to the converter. Operation should be limited to the conditions outlined under the Electrical Specification Section.

Parameter	Min	Max	Unit
Input Voltage (continuous)	-0.5	75	Vdc
Input Voltage (continuous, non-operating)	-	100	Vdc
Operating Ambient Temperature (See Thermal Considerations section)	-40	85*	°C
Storage Temperature	-55	125	°C

*Derating curves provided in this datasheet end at 85°C ambient temperature. Operation above 85°C ambient temperature is allowed provided the temperatures of the key components or the baseplate do not exceed the limit stated in the Thermal Considerations section.

Electrical Specifications

These specifications are valid over the converter's full range of input voltage, resistive load, and temperature unless noted otherwise.

Input Specifications

Parameter	Min	Typical	Max	Unit
Input Voltage	18	36	75	Vdc
Input Current	-	-	3	A
Quiescent Input Current (typical Vin)	-	16	20	mA
Standby Input Current	-	3	5	mA
Input Reflected-ripple Current, peak-to-peak (5 Hz to 20 MHz, 12 µH source impedance)	-	10	-	mA
Input Turn-on Voltage Threshold	16	17	18	V
Input Turn-off Voltage Threshold	14	15	16	V
Input Voltage ON/OFF Control Hysteresis	1	2	4	V

Output Specifications

Parameter	Min	Typical	Max	Unit
Output Voltage Set Point (typical Vin; full load; Ta = 25°C)	-	12.0	-	V
Output Voltage Set Point Accuracy (typical Vin; full load; Ta = 25°C)	-1.5	-	+1.5	%Vo
Output Voltage Set Point Accuracy (over all conditions)	-2.5	-	+2.5	%Vo
Output Regulation:				
Line Regulation (full range input voltage, 1/2 full load)	-	0.05	0.2	%Vo
Load Regulation (full range load, typical Vin)	-	0.05	0.2	%Vo
Temperature (Ta = -40°C to 85 °C)	-	15	50	mV
Output Ripple and Noise Voltage				
RMS	-	20	30	mVrms
Peak-to-peak (5 Hz to 20 MHz bandwidth, typical Vin)	-	140	180	mVp-p
Output Current	0	-	1.5	A
Output Power	0	-	18	W

Output Specifications (Continued)

Parameter	Min	Typical	Max	Unit
Efficiency (typical V_{in} ; full load; $T_a = 25^\circ\text{C}$)	-	88.0	-	%
Output Ripple Frequency	370	400	430	kHz
External Load Capacitance	-	-	500	μF
Startup Delay, duration from enabling signal to V_o reaches 10% of its set point. (typical V_{in} ; full load; $T_a = 25^\circ\text{C}$)	-	5	-	ms
Startup Time, duration for V_o to rise from 10% of its set point to within its regulation band. (typical V_{in} ; full load; $T_a = 25^\circ\text{C}$)	-	15	-	ms
Output Over Current Protection Set Point (full load)	115	-	200	%
Output Over Voltage Protection Set Point (typical V_o)	115	125	140	%
Output Trim Range in % of typical V_o	80	-	110	%
Dynamic Response (typical V_{in} ; $T_a = 25^\circ\text{C}$; load transient $0.1\text{A}/\mu\text{s}$) Load steps from 50% to 75% of full load: Peak deviation Settling time (within 10% band of V_o deviation) Load step from 50% to 25% of full load Peak deviation Settling time (within 10% band of V_o deviation)		3 200		% V_o μs
		3 200		% V_o μs

General Specifications

Parameter	Min	Typical	Max	Unit
Remote Enable Negative Logic: Logic Low-Module On Logic High-Module Off Positive Logic: Logic High-Module On Logic Low-Module Off				
Logic Low: $I_{ON/OFF} = 1.0\text{mA}$ $V_{ON/OFF} = 0.0\text{V}$	0 -	- -	1.2 1.0	V mA
Logic High: $I_{ON/OFF} = 0.0\mu\text{A}$ Leakage Current	3.5 -	- -	15 50	V μA
Isolation Capacitance	-	1,200	-	pF
Insulation Resistance	10	-	-	M Ω
Calculated MTBF (Telecordia SR-332, 2011, Issue 3), full load, 40°C , 60% upper confidence level, typical V_{in}	-	12	-	10^6 -hour

Characteristic Curves

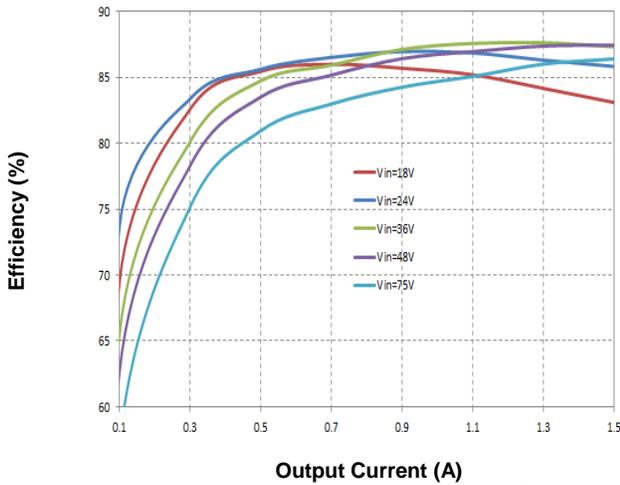


Figure 1. Efficiency vs. Load Current (25°C)

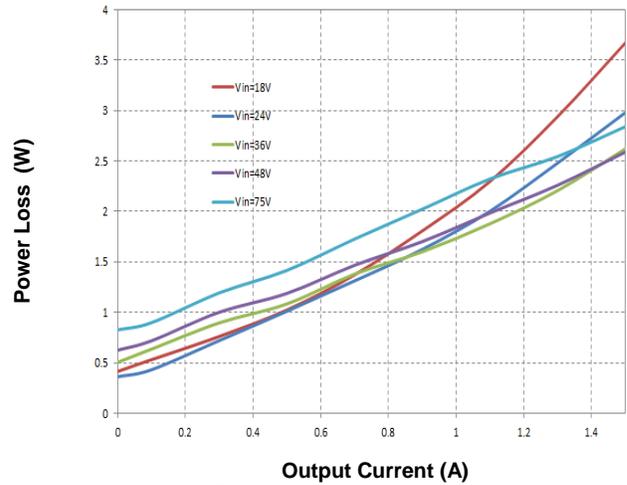


Figure 2. Power Loss vs. Load Current (25°C)

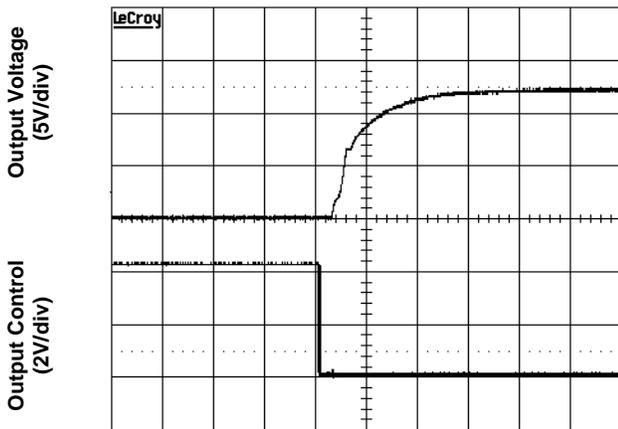


Figure 3. Start-Up from Enable Control (typical V_{in} and full load)

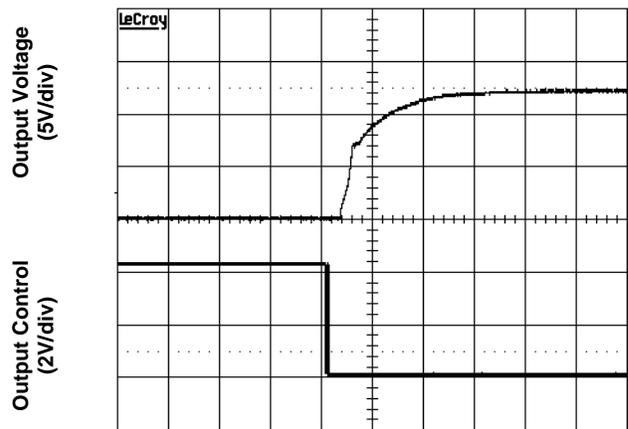


Figure 4. Start-Up from Enable Control (typical V_{in} and zero load)

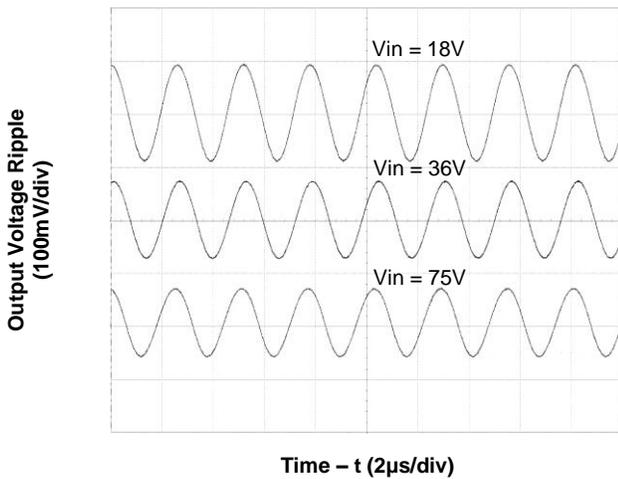


Figure 5. Output Ripple Voltage at Full Load

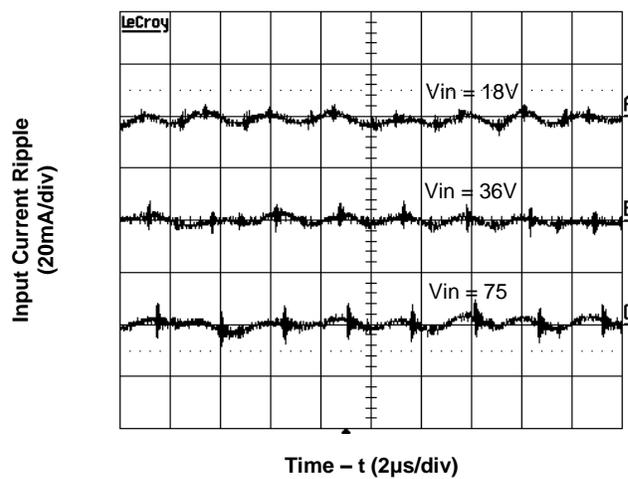


Figure 6. Input Reflected Ripple Current at Full Load

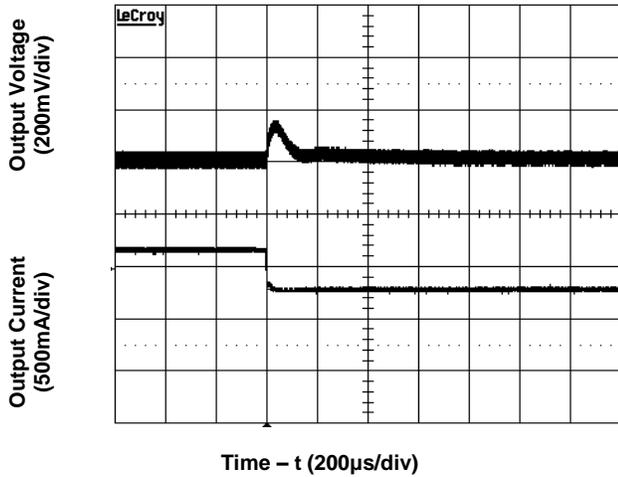


Figure 7. Transient Load Response
(typical V_{in} , load current steps from 50% to 25% at a slew rate 0.1A/ μ s)

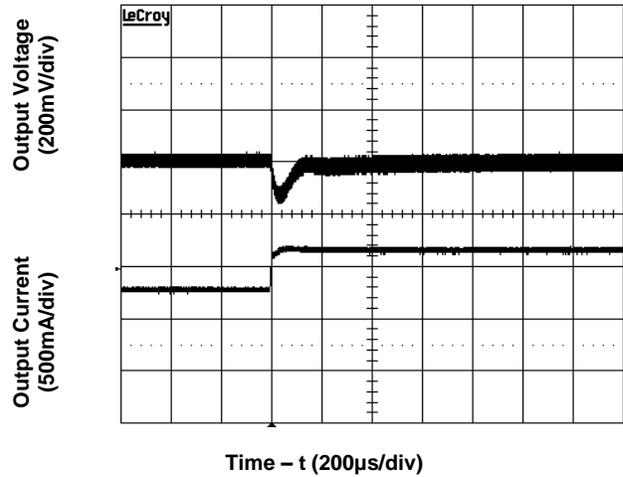


Figure 8. Transient Load Response
(typical V_{in} , load current steps from 50% to 75% at a slew rate 0.1A/ μ s)

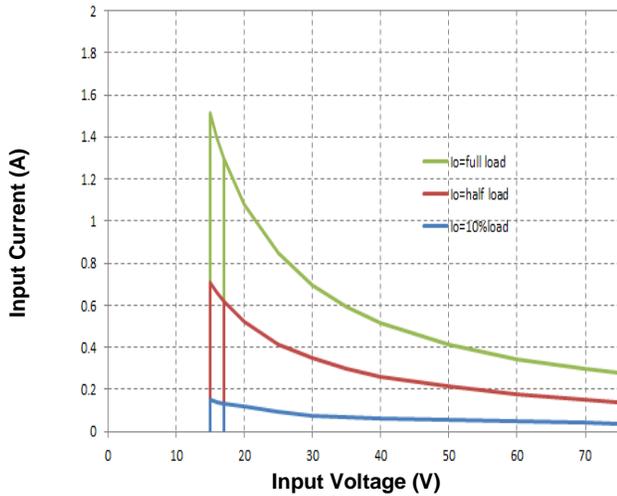


Figure 9. Input Characteristics (25°C)

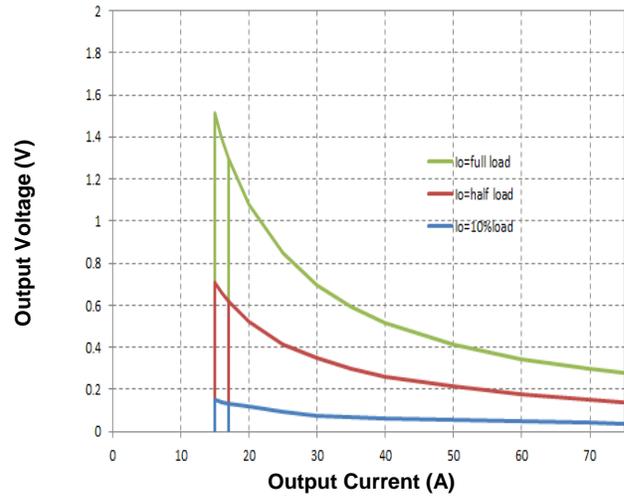


Figure 10. Output Characteristics (25°C)

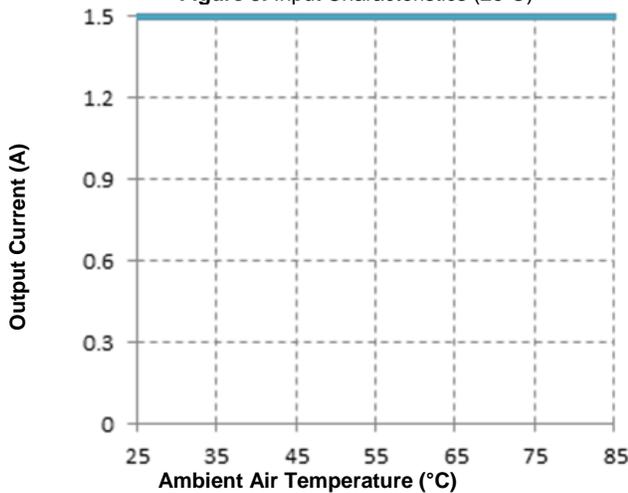


Figure 11. Current Derating Curve under Nature Convection
(Ref. Fig. 12 for Airflow Direction; typical V_{in} , open frame unit, soldered interface)

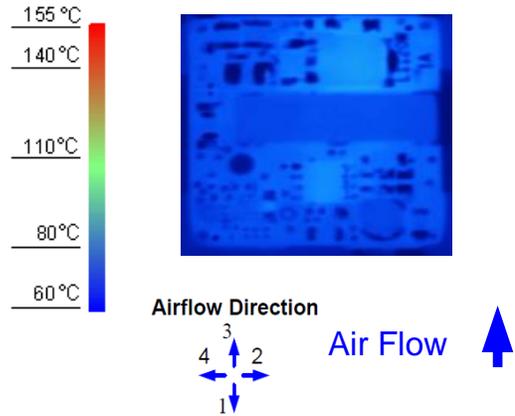


Figure 12. Thermal Image for Airflow Direction 3
(1.5A output, 55°C ambient, 200 LFM, typical V_{in} open frame unit, soldered interface)

Feature Descriptions

Remote ON/OFF

The converter can be turned on and off by changing the voltage between the ON/OFF pin and Vin(-). The LRS3 Series of converters are available with factory selectable positive logic and negative logic.

For the negative control logic, the converter is ON when the ON/OFF pin is at a logic low level and OFF when the ON/OFF pin is at a logic high level. For the positive control logic, the converter is ON when the ON/OFF pin is at a logic high level and OFF when the ON/OFF pin is at a logic low level.

With the internal pull-up circuitry, a simple external switch between the ON/OFF pin and Vin(-) can control the converter. A few example circuits for controlling the ON/OFF pin are shown in Figures 13, 14 and 15.

The logic low level is from 0V to 1.2V and the maximum sink current during logic low is 1mA. The external switch must be capable of maintaining a logic-low level while sinking up to this current. The logic high level is from 3.5V to 15V. The converter has an internal pull-up circuit that ensures the ON/OFF pin at a high logic level when the leakage current at ON/OFF pin is no greater than 50µA.

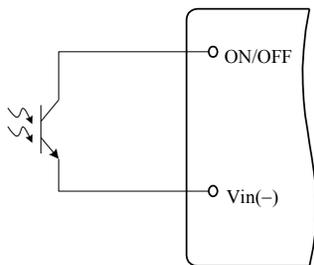


Figure 13. Opto Coupler Enable Circuit

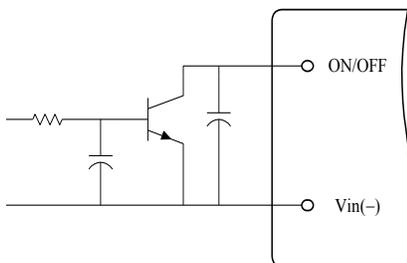


Figure 14. Open Collector Enable Circuit

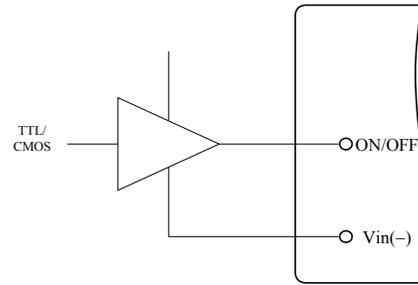


Figure 15. Direct Logic Drive

Output Voltage Adjustment (Trim)

The trim pin allows the user to adjust the output voltage set point. To increase the output voltage, an external resistor is connected between the TRIM pin and SENSE(+). To decrease the output voltage, an external resistor is connected between the TRIM pin and SENSE(-). The output voltage trim range is 80% to 110% of the specified typical output voltage.

The circuit configuration for trim down operation is shown in Figure 16. To decrease the output voltage, the value of the external resistor should be

$$R_{down} = \left(\frac{V_{adj} - 2.5}{12 - V_{adj}} - 0.511 \right) (k\Omega)$$

Where

V_{adj} = Adjusted Output Voltage

The circuit configuration for trim up operation is shown in Figure 17. To increase the output voltage, the value of the resistor should be

$$R_{up} = \left(\frac{2.5 \cdot 10}{V_{adj} - 12} - 0.511 \right) (k\Omega)$$

Where

V_{adj} = Adjusted Output Voltage

As the output voltage at the converter output terminals are higher than the specified typical level when using the trim up and/or remote sense functions, it is important to make sure that the voltage at the output terminals does not exceed the maximum power rating of the converter as given in the Specifications table.

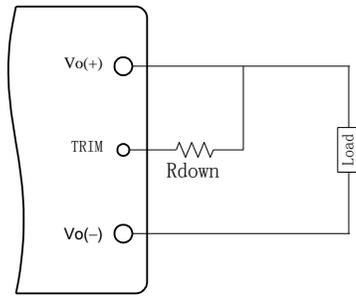


Figure 16. Circuit to Decrease Output Voltage

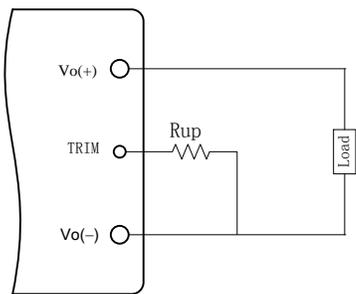


Figure 17. Circuit to Increase Output Voltage

Output Over-Voltage Protection (OVP)

With the latch-off version, the converter will latch off when the output voltage exceeds the limit. The converter can be restarted by toggling the ON/OFF switch or recycling the input voltage.

With the auto-restart version, the converter will operate in a hiccup mode (repeatedly try to restart) until the cause of the over-voltage condition is cleared.

Over Temperature Protection (OTP)

With the latch-off version, the converter will shut down and latch off if an over-temperature condition is detected. The converter has a temperature sensor located at a carefully selected position, which represents the thermal condition of key components of the converter. The thermal shutdown circuit is designed to turn the converter off when the temperature at the sensor reaches 120°C. The module can be restarted by toggling the ON/OFF switch or recycling the input voltage.

With the auto-restart version, the converter will resume operation after the converter cools down.

Input Under-Voltage Lockout

This feature prevents the converter from starting until the input voltage reaches the turn-on voltage threshold, and keeps the converter running until the input voltage falls below the turn-off voltage threshold. Both turn-on and turn-off voltage thresholds are defined in the Input Specifications table. The hysteresis prevents oscillations.

Output Over-Current Protection (OCP)

This converter can be ordered in either latch-off or auto-restart version upon OCP, OVP, and OTP.

With the latch-off version, the converter will latch off when the load current exceeds the limit. The converter can be restarted by toggling the ON/OFF switch or recycling the input voltage.

With the auto-restart version, the converter will operate in a hiccup mode (repeatedly try to restart) until the cause of the over-current condition is cleared.

Design Considerations

As with any DC/DC converter, the stability of the LRS3 converters may be compromised if the source impedance is too high or inductive. It's desirable to keep the input source ac-impedance as low as possible. Although the converters are designed to be stable without adding external input capacitors for typical source impedance, it is recommended to add a 33 μ F low ESR electrolytic capacitors at the input of the converter, which reduces the potential negative impact of the source impedance on the converter stability. These electrolytic capacitors should have sufficient RMS current rating over the operating temperature range.

The converter is designed to be stable without additional output capacitors. To further reduce the output voltage ripple or improve the transient response, additional output capacitors are often used in applications. When additional output capacitors are used, a combination of ceramic capacitors and tantalum/polymer capacitors shall be used to provide good filtering while assuring the stability of the converter.

Safety Considerations

The LRS3 Series of converters are designed in accordance with EN 62368 Safety of Information Technology Equipment Including Electrical Equipment. Flammability ratings of the PWB and plastic components in the converter meet 94V-0.

To protect the converter and the system, an input line fuse is highly recommended on the un-grounded input end.

Thermal Considerations

The LRS3 Series of converters can operate in various thermal environments. Due to the high efficiency and optimal heat distribution, these converters exhibit excellent thermal performance.

The maximum allowable output power of any power converter is usually determined by the electrical design and the maximum operating temperature of its components. The LRS3 Series of converters have been tested comprehensively under various conditions to generate the derating curves with the consideration for long term reliability.

The thermal derating curves are highly influenced by the test conditions. One of the critical variables is the interface method between the converter and the test fixture board. There is no standard method in the industry for the derating tests. Some suppliers use sockets to plug in the converter, while others solder the converter into the fixture board. It should be noted that these two methods produce significantly different results for a given converter. When the converter is soldered into the fixture board, the thermal performance of the converter is significantly improved compared to using sockets due to the reduction of the contact loss and the thermal impedance from the pins to the fixture board. Other factors affecting the results include the board spacing, construction (especially copper weight, holes and openings) of the fixture board and the spacing board, temperature measurement method and ambient temperature measurement point. The thermal derating curves in this datasheet are obtained using a PWB fixture board and a PWB spacing board with no opening, a board-to-board spacing of 1", and the converter is soldered to the test board with thermal relieves.

Note that the natural convection condition was measured at 0.05 m/s to 0.15 m/s (10ft./min. to 30 ft./min).

Heat Transfer

With single-board DC/DC converter designs, convection heat transfer is the primary cooling means for converters without a baseplate. Therefore, airflow speed should be checked carefully for the intended operating environment. Increasing the airflow over the converter enhances the heat transfer via convection.

Figure 18 shows a recommended temperature monitoring point for open frame modules. For reliable operation, the temperature at this location should not continuously exceed 120 °C.

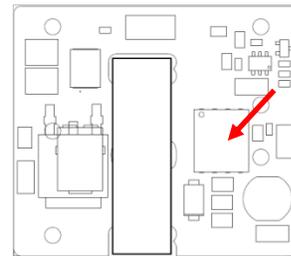
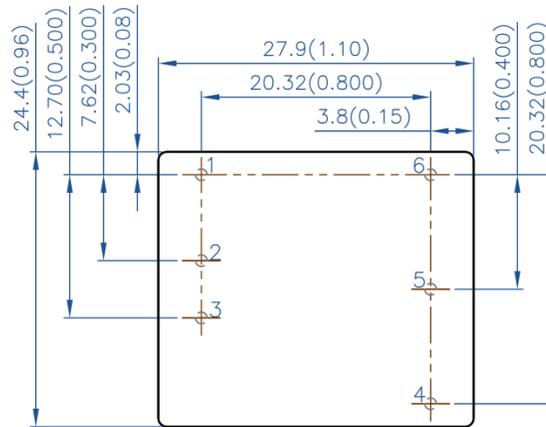


Figure 18. Temperature Monitoring Locations

EMC Considerations

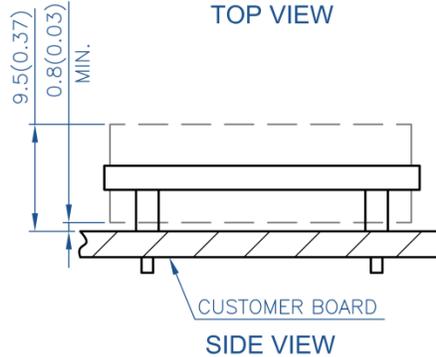
The EMC performance of the converter is related to the layout and filtering design of the customer board. Careful layout and adequate filtering around the converter are important to confine noise generated by the switching actions in the converter and to optimize system EMC performance.

Mechanical Drawing



*All Pins are 1.02mm(0.040) dia.

TOP VIEW



SIDE VIEW

Pin	Name	Function
1	ON/OFF	Remote control
2	Vin(-)	Negative input voltage
3	Vin(+)	Positive input voltage
4	Vout(+)	Positive output voltage
5	TRIM	Output voltage adjustment
6	Vout(-)	Negative output voltage

Notes:

- All dimensions in mm (inches)
Tolerances: .x ± .5 (.xx ± 0.02)
.xx ± .25 (.xxx ± 0.010)
- All pins are 1.02mm (0.040") dia. with +/- 0.10mm (0.004") tolerance; the recommended diameter of the receiving hole is 1.42mm (0.056").
- All pins are coated with 90%/10% solder, Gold, or Matte Tin finish with Nickel under plating.
- Weight: 10 g open frame converter
- Workmanship meets or exceeds IPC-A-610 Class II